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Dated: February 16, 2005

Signature: 

(Peter F. Corless)

Docket No.: 51380(70329)  
(PATENT)

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of:  
Miura Takashi et al.

Application No.: 10/078,766

Confirmation No.: 4845

Filed: February 19, 2002

Art Unit: 1742

For: PROCESS FOR ELECTROPLATING  
SILICON WAFERS

Examiner: W. T. Leader

**REQUEST FOR EXTENSION OF TIME**

MS RCE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Applicant hereby petitions for a four month extension of time to and including February 16, 2005 to respond to the Office Action mailed August 16, 2004.

This petition is being filed in order to ensure copendency with application being filed concurrently herewith.

In the event that a further petition for an extension of time is required to be submitted at this time, applicant hereby petitions under 37 CFR 1.136(a) for an extension of time for as many months as are required to ensure that the above-identified application does not become abandoned.

Please charge our Deposit Account No. 04-1105 in the amount of \$2,380.00 covering the fees set forth in 37 CFR 1.17(a)(4) and 1.17(e). The Director is hereby authorized to charge any deficiency in the fees filed, asserted to be filed or which should have been filed herewith (or with any paper hereafter filed in this application by

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this firm) to our Deposit Account No. 04-1105, under Order No. 51380(70329). A duplicate copy of this paper is enclosed.

Dated: February 16, 2005

Respectfully submitted,

By 

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